

Notice of Allowability	Application No.	Applicant(s)	
	09/961,176	HIYAMA ET AL.	
	Examiner	Art Unit	
	Nitin Patel	2673	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 10/29/2004.
2. ☒ The allowed claim(s) is/are 1-16.
3. ☒ The drawings filed on 24 September 2001 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| <ol style="list-style-type: none"> 1. <input type="checkbox"/> Notice of References Cited (PTO-892) 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | <ol style="list-style-type: none"> 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. 7. <input type="checkbox"/> Examiner's Amendment/Comment 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance 9. <input type="checkbox"/> Other _____. |
|---|--|

REASON FOR ALLOWANCE

1. Claims 1-16 is allowed. Claims 17-22 have been cancelled.
2. The following is an examiner's statement of reason for allowance:

The prior art fails to teach or suggest an image display system having a block discrimination circuit portion for discriminating a state of image information amounting to one frame among the image information in a pixel block unit; an image processing portion for processing the pixel block unit including the image information based on discriminated result of the block discrimination circuit portion; a storage portion for storing image information processed by the image processing portion and a synchronizing signal generation portion for reading the image information from the storage portion, controlling a clock in accordance with the read image information and outputting the read image information to the image display unit and the image processing portion processes the pixel block unit such that either each of a plurality of pixel block areas included in the pixel unit is rewritten for each of a plurality of frames or each pixel block area remains the same for a plurality of the frames as claimed in claims 1,7,9,14.

The prior art fails to teach or suggest a transmitter for transmitting image information having a block discrimination circuit portion for discriminating a state of image information amounting to one frame in a pixel block unit; an image processing portion for processing the pixel block unit including the image information based on discriminated result of the block discrimination circuit portion and a transmitting portion for transmitting the image information the image processing portion processes the pixel

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block unit such that either each of a plurality of pixel block areas included in the pixel unit is rewritten for each of a plurality of frames or each pixel block area remains the same for a plurality of the frames as claimed in claim 11.

The prior art fails to teach or suggest a storage portion for storing image information processed by the image processing portion and a synchronizing signal generation portion for reading the image information from the storage portion, controlling a clock in accordance with the read image information and outputting the read image information to the image display unit and the image processing portion processes the pixel block unit such that either each of a plurality of pixel block areas included in the pixel unit is rewritten for each of a plurality of frames or each pixel block area remains the same for a plurality of the frames as claimed in claim 13.

3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Patel whose telephone number is 571-272-7677. The examiner can normally be reached on 8:00-5:00.

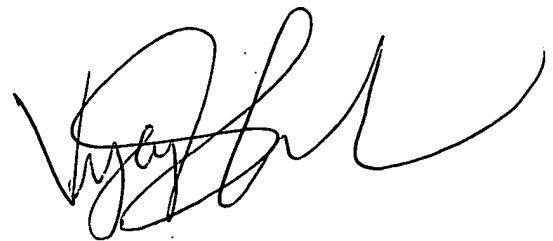
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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin H Shalwala can be reached on 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

NP

April 27, 2005

A handwritten signature in black ink, appearing to read 'Vijay Shankar', with a stylized, flowing script.

**VIJAY SHANKAR
PRIMARY EXAMINER**